ECE/CS 584: Hybrid Automaton Modeling Framework Executions, Reach set, Invariance Lecture 03 Sayan Mitra

Announcements

- Project proposals due in a week
 - 2 pages with goals, description & milestones
- Allerton Conference special session on Verification of CPS
 - October 4th, 1:30 pm at Allerton House

– Free!

Plan for Today

- Examples of hybrid models
- Executions, reach sets, invariants

Hybrid Automata (a.k.a Timed Automata Kaynar, et al. 2005)

 $\mathcal{A}=(X,Q,\Theta,E,H,\mathcal{D},\mathcal{T})$

- X: set of internal or state variables
- $Q \subseteq val(X)$ set of states
- $\Theta \subseteq Q$ set of start states
- *E,H* sets of internal and external actions, A= E U H
- $\mathcal{D} \subseteq Q \times A \times Q$
- \mathcal{T} : set of trajectories for X which is closed under prefix, suffix, and concatenation

Bouncing Ball



Automaton Bouncingball(c,h,g) variables: analog x: Reals := h, v: Reals := 0 states: True actions: external bounce transitions: bounce pre x = 0 / 1 v < 0 yes x = 0 y = 0

Graphical Representation used in many articles

TIOA Specification Language (close to PHAVer & UPPAAL's language)

Semantics: Executions and Traces

- An execution fragment of \mathcal{A} is an (possibly infinite) alternating (A, X)-sequence $\alpha = \tau_0 a_1 \tau_1 a_2 \tau_2 \dots$ where
 - $\forall i \tau_i. lstate \xrightarrow{a_{i+1}} \tau_{i+1}. fstate$
- If τ_0 .fstate $\in \Theta$ then its an execution
- Execs_A set of all executions
- The trace of an execution: external part of the execution. Alternating sequence of external actions and trajectories of the empty set of variables





Special kinds of executions

- Infinite: Infinite sequence of transitions and trajectories
- **Closed**: Finite with final trajectory with closed domain $\psi = \tau_0 \alpha_1 \dots \tau_n$ $\tau_n : [0, t] \rightarrow \sqrt{2}(k)$
- Admissable: Infinite duration
 - May or may not be infinite
- Zeno: Infinite but not admissable
 - Infinite number of transitions in finite time

 $i \sim < 1$

Periodically Sending Process

send(m:M)
 clock = u
 clock := 0



Automaton PeriodicSend(u, M) variables: analog clock: Reals := 0 states: True actions: external send(m:M) transitions:

send(m)

pre clock = u

eff clock := 0

trajectories:

evolve d(clock) = 1
stop when clock=u

Graphical Representation used in many articles

TIOA Specification Language (close to PHAVer & UPPAAL's language)

Another Example: Periodically Sending **Process**



variables: analog clock: Reals := 0, z:Reals, failed:Boolean := F actions: external send(m:Reals), fail

> **pre** clock = $u \land m = z \land ~$ failed eff clock := 0

pre true

eff failed := T

evolve d(clock) = 1, d(z) = f(z)**stop when** ~failed /\ clock=u

Inv : $S \subseteq Q$ $4t' \in \Upsilon.dom$ $5top : S \subseteq Q$ $4t' \in \Upsilon.dom$ $\gamma(t') \in S \implies t' = \Upsilon.llime$ $4t' \in \Upsilon.dom$

Modeling a Simple Failure Detector System

- Periodic send
- Channel
- Timeout

\checkmark	send(m)		recv(m)		-r
send	-> Cha	nnel		FD	
feil				\bigvee	1
}				Juspe	ct

Time bounded channel & Simple Failure Detector

```
Automaton Channel(b,M)
Automaton Timeout(u,M)
 variables: suspected: Boolean := F,
                                           variables: queue: Queue[M,Reals] := {}
         clock: Reals := 0
                                                   clock: Reals := 0
 actions: external receive(m:M),
                                           actions: external send(m:M), receive(m:M)
timeout
                                           transitions:
 transitions:
                                              send(m)
    receive(m)
                                              pre true
    pre true
                                              eff queue := append(<m, clock+b>, queue)
    eff clock := 0; suspected := false;
                                              receive(m)√
    timeout
                                              pre head(queue)[1] = m
    pre \sim suspected /\ clock = u
                                              eff queue := queue.tail
                             (1-f) ≤ d(clock trajectories:
    eff suspected := true
 trajectories:
                                              evolve d(clock) = 1
    evolve d(clock) = 1
                                              stop when \exists m, d, \langlem,d\rangle \in queue
    stop when clock = u / \sim suspected
                                                   \land clock=d
```

Reachable States and Invariants

- A state v ∈ Q is reachable if there exists an execution α with α.lstate = v
- *Reach*_A Set of all reachable states
- An S $\subseteq Q$ is an **invariant** if **Reach**_A \subseteq S
 - Generalizes the idea of conservation
- So, any invariant necessarily contains the set Θ of start states

- Examples:
 - Bouncing ball: $h \ge x \ge 0$
 - $\circ 0 < v^2 \le 2g(h-x)$
 - Periodic send: ~failed
 ⇒ clock ≤ u

Example Inductive Invariance Proof

- Invariant, For $\mathbf{x} \in \text{Reach}_{\text{TC}}$: $\forall < m,d > \in x.queue: x.clock \leq d \leq x.clock+b$ (1)Proof. Fix $\mathbf{x} \in \text{Reach}_{TC}$. ullet $\exists \alpha \in \text{Exec}_{\text{TC}} \text{ with } \alpha \text{.lstate} = \mathbf{x}. \text{ Fix } \alpha = \tau_0 a_1 \tau_1 a_2 \dots \tau_n. \text{ [Def. Reach}_{\text{TC}} \text{]}$ • Induction on the length of the execution ${\color{black}\bullet}$ Base case: If we set $\mathbf{x} = \tau_0$. fstate then (1) should hold lacksquare– Holds vacuously as x.queue = {} [Def of initial states] Inductive step 1: Consider any τ , let **x** = τ .fstate and **x'** = τ .lstate and lacksquare τ .ltime = t. Assume **x** satisfies (1) and show that **x'** also. - x.queue = x'.queue [trajectory Def], Fix <m,d> in x.queue - **x**.clock \leq d [By Assumption] - Suppose \mathbf{x} '.clock > d - \mathbf{x} .clock - \mathbf{x} .clock > d - \mathbf{x} .clock - t > d - **x**-clock, then there exists $t' \in \tau$.dom and t' < t where $\tau(t')$.clock = d - By **stop when** τ .ltime = t' which is a contradiction - Also, since $d \leq \mathbf{x}$.clock+b, $d \leq \mathbf{x'}$.clock+t+b
- Inductive step 2: Consider $x-send(m) \rightarrow x'$
- Inductive step 3: Consider x—receive(m) $\rightarrow x'$ follows from Assumption.

Inductive Invariants

- An invariant S is inductive if for any $v \in S$
 - If v—a \rightarrow v' then v' \in S
 - If $v \rightarrow \tau \rightarrow v'$ then $v' \in S$
- Proof rule for establishing an inductive invariant S
- Theorem: For any set of states S if
 - 1. for any $v \in \Theta$ start state, $v \in S$
 - 2. If $v \in S$ and $v a \rightarrow v'$ then $v' \in S$
 - 3. If $v \in S$ and $v \tau \rightarrow v'$ then $v' \in S$

Then $\operatorname{Reach}_{\mathcal{A}} \subseteq S$

Pre and Post Computations

- For a given set of states $Q' \subseteq Q$, and action $a \in A$
 - Post_trans(Q', a) = { v' | $\exists v \in Q', v \rightarrow v'$ }
 - Post_trans(Q', A') = { v' | $\exists v \in Q', a \in A, v \rightarrow v'$ }
 - Post_taj(Q') = { v' | $\exists v \in Q', \tau \in T, v \tau \rightarrow v'$ }
 - $Post(Q') = Post_trans(Q', A) \cup Post_taj(Q')$
 - $-\operatorname{Pre_trans}(Q', A') = \{ v \mid \exists v' \in Q', a \in A, v a \rightarrow v' \}$
 - Pre_taj(Q') = { v | \exists v's \in Q', $\tau \in$ T, v— $\tau \rightarrow$ v'}
 - $Pre(Q') = Pre_trans(Q', A) \cup Pre_taj(Q')$

Characteristics of Timed Automata

- Guards, Transition relations, Invariants, DAEs written in some language
- These objects define the Transitions and Trajectories
- Transitions and trajectories define executions and traces
- Decidability of verification problem will depend on the choice of the language
- Nondeterministic
 - Transition choice
 - Transition relation
 - Branching trajectories
- External interface
 - External actions
 - Further partitioned into I/O actions
 - External variables available in the hybrid I/O automaton model

- Special cases
 - Deterministic HA
 - Rectangular HA
 - (Alur-Dill) Timed Automata
 - X = Finitely many variables with finite types → Finite State Machine with Labeled transitions
 - X = n real valued variables $\{x1, ..., xn\}$ and A = $\{\}$ D = $\{\} \rightarrow$ Dynamical System

Summary & Roadmap

- Hybrid Automata
- Syntax
- Executions
- Reach sets, Invariance
- Abstractions,
 Simulations and
 Composition